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13691

09/870,767

Richard S. NORMAN et al.

June 1, 2001

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PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

[illegible]

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	5.	<p>NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987</p>
	6.	<p>International Search Report PCT/CA02/00810, August 1, 2003</p>

EXAMINER

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8/1/05

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INFORMATION DISCLOSURE CITATION
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Docket Number (Optional)

13691

Application Number

09/870,767

Applicant(s)

Richard S. Norman et al.

Filing Date

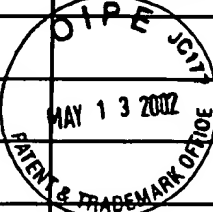
06/01/2001

Group Art Unit

2664

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*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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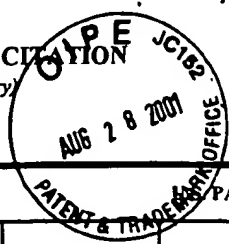
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Docket Number (Optional) 13691	Application Number 09/870,767
Applicant(s) Richard NORMAN et al.	
Filing Date June 1, 2001	Group Art Unit 2661 2664

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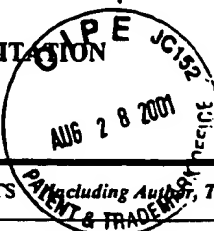
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INFORMATION DISCLOSURE CITATION
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Docket Number (Optional)

136

Application Number

09, 870, 767

Applicant(s)

Richard S. NORMAN et al.

Filing Date

6/1/91

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m	13.	Nick McKeown et al. "The Tiny Tera: a Packet Switch Core"; Departments of Electrical Engineering and Computer Science, Stanford University; August 1996; http://tiny-tera.stanford.edu/tiny-tera/index.html

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r	15.	U.s. 5,859,835	12/01/1999	Varma et al.	—	✓	

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